

## References

- [1] F.S. de Boer. A hoare logic for dynamic networks of asynchronously communicating deterministic processes. *Theor. Comput. Sci.*, 274(1-2):3–41, 2002.
- [2] Holger Hermanns, Ulrich Herzog, and Joost-Pieter Katoen. Process algebra for performance evaluation. *Theor. Comput. Sci.*, 274(1-2):43–87, 2002.
- [3] D. Lugiez and Ph. Schnoebelen. The regular viewpoint on pa-processes. *Theor. Comput. Sci.*, 274(1-2):89–115, 2002.
- [4] P. Madhusudan and P.S. Thiagarajan. Branching time controllers for discrete event systems. *Theor. Comput. Sci.*, 274(1-2):117–149, 2002.
- [5] P.-Y. Schobbens, J.-F. Raskin, and T.A. Henzinger. Axioms for real-time logics. *Theor. Comput. Sci.*, 274(1-2):151–182, 2002.
- [6] Peter Sewell. From rewrite rules to bisimulation congruences. *Theor. Comput. Sci.*, 274(1-2):183–230, 2002.
- [7] Nobuko Yoshida. Minimality and separation results on asynchronous mobile processes — representability theorems by concurrent combinators. *Theor. Comput. Sci.*, 274(1-2):231–276, 2002.