References

[1] Marco A.Z. Alves, Henrique C. Freitas, and Philippe O.A. Navaux. High latency and contention on shared l2-cache for many-core architectures. *Parallel Processing Letters*, 21(1):85–106, 2011.

[2] Pavan Balaji, Darius Buntinas, David Goodell, William Gropp, Torsten Hoefler, Sameer Kumar, Ewing Lusk, Rajeev Thakur, and Jesper Larsson Träff. Mpi on millions of cores. *Parallel Processing Letters*, 21(1):45–60, 2011.

[3] Nazreen Banu, Taisuke Izumi, and Koichi Wada. Adaptive and doubly-expedited one-step consensus in byzantine asynchronous systems. *Parallel Processing Letters*, 21(4):461–477, 2011.

[4] Franck Cappello, Henri Casanova, and Yves Robert. Preventive migration vs. preventive checkpointing for extreme scale supercomputers. *Parallel Processing Letters*, 21(2):111–132, 2011.

[5] Pietro Ceres and Raffaele Ceres. On the fault-tolerance and size of wdm optical networks. *Parallel Processing Letters*, 21(1):3–12, 2011.

[6] Eddie Cheng, Ke Qiu, and Zhizhang Shen. On the surface areas and average distances of meshes and tori. *Parallel Processing Letters*, 21(1):61–75, 2011.

[7] I-Hsin Chung, Che-Rung Lee, Jiazheng Zhou, and Yeh-Ching Chung. Hierarchical mapping for hpc applications. *Parallel Processing Letters*, 21(3):279–299, 2011.

[8] David Clarke, Alexey Lastovetsky, and Vladimir Rychkov. Dynamic load balancing of parallel computational iterative routines on highly heterogeneous hpc platforms. *Parallel Processing Letters*, 21(2):195–217, 2011.

[9] Sékou Diakité, Loris Marchal, Jean-Marc Nicod, and Laurent Philippe. Practical steady-state scheduling for tree-shaped task graphs. *Parallel Processing Letters*, 21(4):397–412, 2011.

[10] Alejandro Duran, Eduard Ayguadé, Rosa M. Badia, Jesús Labarta, Luis Martinell, Xavier Martorell, and Judit Planas. Ompss: A proposal for

programming heterogeneous multi-core architectures. *Parallel Processing Letters*, 21(2):173–193, 2011.

[11] Niall Emmart and Charles C. Weems. High precision integer multiplication with a gpu using strassen's algorithm with multiple fft sizes. *Parallel Processing Letters*, 21(3):359–375, 2011.

[12] Carlos Fernando Gamboa and Thomas Robertazzi. Simple performance bounds for multicore and parallel channel systems. *Parallel Processing Letters*, 21(4):439–460, 2011.

[13] Darren J. Kerbyson and Kevin J. Barker. Modeling the performance of direct numerical simulation on parallel systems. *Parallel Processing Letters*, 21(3):301–318, 2011.

[14] Cheng-Chi Lee, Chun-Ta Li, and Shun-Der Chen. Two attacks on a two-factor user authentication in wireless sensor networks. *Parallel Processing Letters*, 21(1):21–26, 2011.

[15] Duane Merrill and Andrew Grimshaw. High performance and scalable radix sorting: A case study of implementing dynamic parallelism for gpu computing. *Parallel Processing Letters*, 21(3):245–272, 2011.

[16] Ana-Maria Oprescu, Thilo Kielmann, and Haralambie Leahu. Budget estimation and control for bag-of-tasks scheduling in clouds. *Parallel Processing Letters*, 21(3):219–243, 2011.

[17] Anne-Cecile Orgerie and Laurent Lefevre. Eridis: Energy-efficient reservation infrastructure for large-scale distributed systems. *Parallel Processing Letters*, 21(2):133–154, 2011.

[18] Tien-Tai Pan and Shun-Shii Lin. The transitive closure and related algorithms of digraph on the reconfigurable architecture. *Parallel Processing Letters*, 21(1):27–43, 2011.

[19] Thomas G. Robertazzi. A product form solution for tree networks with divisible loads. *Parallel Processing Letters*, 21(1):13–20, 2011.

[20] Gerald Schubert, Holger Fehske, Georg Hager, and Gerhard Wellein. Hybrid-parallel sparse matrix-vector multiplication with explicit communication overlap on current multicore-based systems. *Parallel Processing Letters*, 21(3):339–358, 2011.

[21] Yanhua Sun, Gengbin Zheng, Pritish Jetley, and Laxmikant V. Kale. Parssse: An adaptive parallel state space search engine. *Parallel Processing Letters*, 21(3):319–338, 2011.

[22] Shiang-Feng Tzeng, Cheng-Chi Lee, and Min-Shiang Hwang. A batch verification for multiple proxy signature. *Parallel Processing Letters*, 21(1):77–84, 2011.

[23] M. Irfan Uddin, Michiel W. van Tol, and Chris R. Jesshope. High level simulation of svp many-core systems. *Parallel Processing Letters*, 21(4):413–438, 2011.

[24] Blesson Varghese, Gerard Mckee, and Vassil Alexandrov. Can agent intelligence be used to achieve fault tolerant parallel computing systems? *Parallel Processing Letters*, 21(4):379–396, 2011.

[25] Coral Walker, Dashan Lu, and David W. Walker. Automatic portal generation based on workflow description. *Parallel Processing Letters*, 21(2):155–171, 2011.