References

[1] Yaagoub A. Ashir and Iain A. Stewart. On embedding cycles in k-ary n-cubes. *Parallel Processing Letters*, 7(1):49–55, 1997.

[2] Nadjib Badache and Aomar Maddi. Gradual design of a causal broadcast protocol. *Parallel Processing Letters*, 7(3):309–320, 1997.

[3] Maurelio Boari, Antonio Corradi, Cesare Stefanelli, and Letizia Leonardi. A routing strategy for object-oriented applications in massively parallel architectures. *Parallel Processing Letters*, 7(3):237–247, 1997.

[4] J.-P. Bodeveix and M. Filali. Towards the automatic verification of atomic memory protocols. *Parallel Processing Letters*, 7(1):101–112, 1997.

[5] Timothy Brecht, Xiaotie Deng, and Nian Gu. Competitive dynamic multiprocessor allocation for parallel applications. *Parallel Processing Letters*, 7(1):89–100, 1997.

[6] Chihming Chang and Rami Melhem. Arbitrary size benes networks. *Parallel Processing Letters*, 7(3):279–284, 1997.

[7] Jean-François Collard and Martin Griebl. Array dataflow analysis for explicitly parallel programs. *Parallel Processing Letters*, 7(2):117–131, 1997.

[8] Alain Darte, Georges-André Silber, and Frédéric Vivien. Combining retiming and scheduling techniques for loop parallelization and loop tiling. *Parallel Processing Letters*, 7(4):379–392, 1997.

[9] Alain Darte and Frédéric Vivien. Parallelizing nested loops with approximation of distance vectors: A survey. *Parallel Processing Letters*, 7(2):133–144, 1997.

[10] Malika De and Bhabani P. Sinha. Fast parallel multiplication using redundant quarternary number system. *Parallel Processing Letters*, 7(1):13–23, 1997.

[11] Mourad Debbabi. A model-based concurrent specification language over cml: Semantic foundations. *Parallel Processing Letters*, 7(3):329–356, 1997.

[12] Michele Flammini. On the hardness of devising interval routing schemes. *Parallel Processing Letters*, 7(1):39–47, 1997.

[13] Ronald I. Greenberg, Shih-Chuan Hung, and Jau-Der Shih. Parallel algorithms for single-layer channel routing. *Parallel Processing Letters*, 7(3):267–277, 1997.

[14] Manish Gupta and Edith Schonberg. Static analysis to reduce synchronization costs — data-parallel programs with remote memory copy. *Parallel Processing Letters*, 7(2):145–156, 1997.

[15] Daniel S. Hirschberg and Lynn M. Stauffer. Dictionary compression on the pram. *Parallel Processing Letters*, 7(3):297–308, 1997.

[16] F.K. Hwang, Tzai-Shunne Lin, and Rong-Hong Jan. A permutation routing algorithm for double loop networks. *Parallel Processing Letters*, 7(3):259–265, 1997.

[17] Wesley K. Kaplow and Boleslaw K. Szymanski. Compile-time cache performance prediction and its application to tiling. *Parallel Processing Letters*, 7(4):393–407, 1997.

[18] Jin S. Kim, Seung Ryoul Maeng, and H. Yoon. Ring embedding in hypercubes with faculty nodes. *Parallel Processing Letters*, 7(3):285–296, 1997.

[19] Ulrich Kremer. Optimal and near-optimal solutions for hard compilation problems. *Parallel Processing Letters*, 7(4):371–378, 1997.

[20] Chi-Chung Lam, P. Sadayappan, and Rephael Wenger. On optimizing a class of multi-dimensional loops with reduction for parallel execution. *Parallel Processing Letters*, 7(2):157–168, 1997.

[21] Shahram Latifi and Ramesh Gajjala. Reliability evaluation of braided networks using a recursive method. *Parallel Processing Letters*, 7(1):77–88, 1997.

[22] Hyuk-Jae Lee and José A.B. Fortes. Modular mappings and data distribution — independent computations. *Parallel Processing Letters*, 7(2):169–180, 1997.

[23] Andrzej Lingas and Anil Maheshwari. A simple optimal parallel algorithm for reporting paths in a tree. *Parallel Processing Letters*, 7(1):3–11, 1997.

[24] Adam Malinowski. Efficient byzantine agreement in networks with random faults. *Parallel Processing Letters*, 7(1):69–76, 1997.

[25] Yunheung Paek and David A. Padua. Compiling for scalable multiprocessors with polaris. *Parallel Processing Letters*, 7(4):425–436, 1997.

[26] Petrişor Panaite. Routing permutations on a 2d grid with one-way edges. *Parallel Processing Letters*, 7(3):225–235, 1997.

[27] Marina Papatriantafilou and Philippas Tsigas. On self-stabilizing wait-free clock synchronization. *Parallel Processing Letters*, 7(3):321–328, 1997.

[28] Patrice Quinton, Sanjay Rajopadhye, and Tanguy Risset. On manipulating z-polyhedra using a canonical representation. *Parallel Processing Letters*, 7(2):181–194, 1997.

[29] Sanguthevar Rajasekaran and Sartaj Sahni. Deterministic routing on the array with reconfigurable optical buses. *Parallel Processing Letters*, 7(3):219–224, 1997.

[30] A.A. Rescigno. Fault-tolerant parallel communication int the star network. *Parallel Processing Letters*, 7(1):57–68, 1997.

[31] Robert Schreiber. High performance fortran, version 2. *Parallel Processing Letters*, 7(4):437–449, 1997.

[32] Paul Spirakis and Vassilis Triantafillou. Pure greedy hot-potato routing in the 2-d mesh with random destinations. *Parallel Processing Letters*, 7(3):249–258, 1997.

[33] A. Venkatachar, J. Ramanujam, and A. Thirumalai. Communication generation for block-cyclic distributions. *Parallel Processing Letters*, 7(2):195–202, 1997.

[34] D. Wilde and S. Rajopadhye. Memory reuse analysis in the polyhedral model. *Parallel Processing Letters*, 7(2):203–215, 1997.

[35] Jingling Xue. On tiling as a loop transformation. *Parallel Processing Letters*, 7(4):409–424, 1997.

[36] Xin Yuan, Rajiv Gupta, and Rami Melhem. Demand-driven data flow analysis for communication optimization. *Parallel Processing Letters*, 7(4):359–370, 1997.

[37] Christos D. Zaroliagis. Simple and work-efficient parallel algorithms for the minimum spanning tree problem. *Parallel Processing Letters*, 7(1):25–37, 1997.