

## References

- [1] Dominique Ambroise and Brigitte Rozoy. Marrella: A tool to analyse the graph of states. *Parallel Processing Letters*, 6(4):583–594, 1996.
- [2] Chris Armen and Donald B. Johnson. Deterministic leader election on the asynchronous qrqw pram. *Parallel Processing Letters*, 6(2):247–250, 1996.
- [3] M.P. Bekakos and O.B. Efremides. A class of systolic tridiagonal linear solvers. *Parallel Processing Letters*, 6(3):355–364, 1996.
- [4] Yosi Ben-Asher and Assaf Schuster. Low crosstalk address encodings for optical message switching systems. *Parallel Processing Letters*, 6(1):87–100, 1996.
- [5] Yosi Ben-Asher and Assaf Schuster. Time-size tradeoffs for reconfigurable meshes. *Parallel Processing Letters*, 6(2):231–245, 1996.
- [6] Dany Breslauer and Ramesh Hariharan. Optimal parallel construction of minimal suffix and factor automata. *Parallel Processing Letters*, 6(1):35–44, 1996.
- [7] David Cachera and Gil Utard. Proving data-parallel programs: A unifying approach. *Parallel Processing Letters*, 6(4):491–505, 1996.
- [8] J. Allen Carruth and Jayadev Misra. Proof of a real-time mutual-exclusion algorithm. *Parallel Processing Letters*, 6(2):251–257, 1996.
- [9] Shiva Chaudhuri, Yannis Dimopoulos, and Christos D. Zaroliagis. On the parallel complexity of acyclic logic programs. *Parallel Processing Letters*, 6(2):223–230, 1996.
- [10] Danny Z. Chen and Xiaobo (Sharon) Hu. Fast and efficient operations on parallel priority queues. *Parallel Processing Letters*, 6(4):451–467, 1996.
- [11] Kuo-Liang Chung. Image template matching on reconfigurable meshes. *Parallel Processing Letters*, 6(3):345–353, 1996.

- [12] Jose M. Claver, Vicente Hernandez, and Enrique S. Quintana. Solving discrete-time lyapunov equations for the cholesky factor on a shared memory multiprocessor. *Parallel Processing Letters*, 6(3):365–376, 1996.
- [13] Alain Darté, Michèle Dion, and Yves Robert. A characterization of one-to-one modular mappings. *Parallel Processing Letters*, 6(1):145–157, 1996.
- [14] Giulio Destri and Paolo Marenzoni. Benchmarking lattice-based applications on parallel architectures. *Parallel Processing Letters*, 6(3):309–320, 1996.
- [15] Luis Díaz de Cerio, Antonio González, and Miguel Valero-García. Communication pipelining in hypercubes. *Parallel Processing Letters*, 6(4):507–523, 1996.
- [16] Marios D. Dikaiakos, Anne Rogers, and Kenneth Steiglitz. Functional algorithm simulation of the fast multipole method: Architectural implications. *Parallel Processing Letters*, 6(1):55–66, 1996.
- [17] Clementin Tayou Djamegni and Maurice Tchuente. Scheduling of the dag associated with pipeline inversion of triangular matrices. *Parallel Processing Letters*, 6(1):13–26, 1996.
- [18] Ronald Fernandes and Arkady Kanevsky. On recursive interconnection networks and their extensions. *Parallel Processing Letters*, 6(3):377–388, 1996.
- [19] Paolo Ferragina and Fabrizio Luccio. Three techniques for parallel maintenance of a minimum spanning tree under batch of updates. *Parallel Processing Letters*, 6(2):213–222, 1996.
- [20] Martin Fürer and Balaji Raghavachari. Parallel edge coloring approximation. *Parallel Processing Letters*, 6(3):321–329, 1996.
- [21] Clive N. Galley. An  $\omega(\log n - k \log k)$  time linear cost lower bound for the  $k$  functions coarsest partition problem. *Parallel Processing Letters*, 6(2):195–202, 1996.
- [22] Jordi Garcia, Eduard Ayguadé, and Jesús Labarta. Using a 0-1 integer programming model for automatic static data distribution. *Parallel Processing Letters*, 6(1):159–171, 1996.

- [23] Zully N. Grant-Duff and Peter G. Harrison. Parallelism via homomorphisms. *Parallel Processing Letters*, 6(2):279–295, 1996.
- [24] Brian Grayson and Robert van de Geijn. A high performance parallel strassen implementation. *Parallel Processing Letters*, 6(1):3–12, 1996.
- [25] Qian-Ping Gu and Shietung Peng. Fault tolerant routing in hypercubes and star graphs. *Parallel Processing Letters*, 6(1):127–136, 1996.
- [26] Ajay K. Gupta and Garrison W. Greenwood. Applications of evolutionary strategies to fine-grained task scheduling. *Parallel Processing Letters*, 6(4):551–561, 1996.
- [27] Christoph Herrmann and Christian Lengauer. On the space-time mapping of a class of divide-and-conquer recursions. *Parallel Processing Letters*, 6(4):525–537, 1996.
- [28] André Hily and Dominique Sotteau. Gossiping in  $d$ -dimensional mesh-bus networks. *Parallel Processing Letters*, 6(1):101–113, 1996.
- [29] Costas S. Iliopoulos and Maureen Korda. Optimal parallel superprimivity testing for square arrays. *Parallel Processing Letters*, 6(3):299–308, 1996.
- [30] Josph Jájá and Kwan Woo Ryu. An optimal randomized parallel algorithm for the single function coarsest partition problem. *Parallel Processing Letters*, 6(2):187–193, 1996.
- [31] Claude Jard and Guy-Vincent Jourdan. Incremental transitive dependency tracking in distributed computations. *Parallel Processing Letters*, 6(3):427–435, 1996.
- [32] Wesley K. Kaplow and Boleslaw K. Szymanski. Program optimization based on compile-time cache performance prediction. *Parallel Processing Letters*, 6(1):173–184, 1996.
- [33] Geunmo Kim and Hyunsoo Yoon. Free submesh list strategy: A best fit submesh allocation in mesh connected multicomputers. *Parallel Processing Letters*, 6(1):75–86, 1996.

- [34] David M. Koppelman. A lower bound on the average physical length of edges in the physical realization graphs. *Parallel Processing Letters*, 6(1):137–143, 1996.
- [35] Ding-Ming Kwai and Behrooz Parhami. A generalization of hypercubic networks based on their chordal ring structures. *Parallel Processing Letters*, 6(4):469–477, 1996.
- [36] Y. le Guyadec and B. Virot. Sequential-like proofs of data-parallel programs. *Parallel Processing Letters*, 6(3):415–426, 1996.
- [37] Bu-Sung Lee, Alfred Heng, Wentong Cai, and Tai-Ann Tan. Task scheduling facility for pvm. *Parallel Processing Letters*, 6(4):563–574, 1996.
- [38] Masaaki Mizuno, Mitchell L. Neilsen, and Michel Raynal. An optimistic protocol for a linearizable distributed shared memory system. *Parallel Processing Letters*, 6(2):265–278, 1996.
- [39] David M. Nicol and Weizhen Mao. On bottleneck partitioning of  $k$ -ary  $n$ -cubes. *Parallel Processing Letters*, 6(3):389–399, 1996.
- [40] Madhumangal Pal and G.P. Bhattacharjee. An optimal parallel algorithm to color an interval graph. *Parallel Processing Letters*, 6(4):439–449, 1996.
- [41] Krishnamurthi Ravishankar and Suresh Singh. Gossiping on a ring with radios. *Parallel Processing Letters*, 6(1):115–126, 1996.
- [42] Jean-Louis Roch and Gilles Villard. Fast parallel computation of the jordan normal form of matrices. *Parallel Processing Letters*, 6(2):203–212, 1996.
- [43] Chris Scheiman and Peter Cappello. A processor-time-minimal design for 3d rectilinear mesh algorithms. *Parallel Processing Letters*, 6(4):539–550, 1996.
- [44] Hava T. Siegelmann. On nil: The software constructor of neural networks. *Parallel Processing Letters*, 6(4):575–582, 1996.
- [45] Ambuj K. Singh. Bounded timestamps in process networks. *Parallel Processing Letters*, 6(2):259–264, 1996.

- [46] Pradip K. Srimani. Super rotator: Incrementally extensible directed network graph of sublogarithmic diameter. *Parallel Processing Letters*, 6(4):479–490, 1996.
- [47] Ivan Stojmenovic. Generating  $n$ -ary reflected gray codes on a linear array of processors. *Parallel Processing Letters*, 6(1):27–34, 1996.
- [48] A.A. Veglis and A.S. Pombortsis. Memory bandwidth analysis of shared-memory multiprocessors using dynamic request rate. *Parallel Processing Letters*, 6(1):67–74, 1996.
- [49] Biing-Feng Wang. A better analysis of ben-asher’s algorithm for the conditional cartesian product problem. *Parallel Processing Letters*, 6(3):331–344, 1996.
- [50] Min-You Wu. A parallel scheduling algorithm for tree structured interconnection networks. *Parallel Processing Letters*, 6(1):45–54, 1996.
- [51] Jingling Xue. Generalising the unimodular approach to restructure imperfectly nested loops. *Parallel Processing Letters*, 6(3):401–414, 1996.