References

[1] Selim G. Akl and Hartmut Schmeck. Systolic sorting in a sequential input/output environment. *Parallel Computing*, 3(1):11–23, 1986.

[2] Tim S. Axelrod. Effects of synchronization barriers on multiprocessor performance. *Parallel Computing*, 3(2):129–140, 1986.

[3] O. Axelsson and V. Eijkhout. A note on the vecotrization of scalar recursions. *Parallel Computing*, 3(1):73–83, 1986.

[4] Daniel Boley. Solving the generalized eigenvalue problem on a synchronous linear processor array. *Parallel Computing*, 3(2):153–166, 1986.

[5] A. Brass and G.S. Pawley. Two and three dimensinal ffts on highly parallel computers. *Parallel Computing*, 3(2):167–184, 1986.

[6] J.C. Browne. Framework for formulation and analysis of parallel computation structures. *Parallel Computing*, 3(1):1–9, 1986.

[7] B.L. Buzbee. A strategy for vectorization. *Parallel Computing*, 3(3):187–192, 1986.

[8] J.J. Dongarra, A.H. Sameh, and D.C. Sorensen. Implementation of some concurrent algorithms for matrix factorization. *Parallel Computing*, 3(1):25–34, 1986.

[9] Iain S. Duff. Parallel implementation of multifrontal schemes. *Parallel Computing*, 3(3):193–204, 1986.

[10] D.J. Evans and G.M. Megson. Romberg integration using systolic arrays. *Parallel Computing*, 3(4):289–304, 1986.

[11] D.J. Evans and Nadia Y. Yousif. The parallel neighbour sort and 2-way merge algorithm. *Parallel Computing*, 3(1):85–90, 1986.

[12] V. Faber, Olaf M. Lubeck, and Jr. White, Andrew B. Superlinear speedup of an efficient sequential algorithm is not possible. *Parallel Computing*, 3(3):259–260, 1986.

[13] Dennis Gannon and Jairo Panetta. Restructuring simple for the chip architecture. *Parallel Computing*, 3(4):305–326, 1986.

[14] Michael Goldapp. Fast scan-line conversion using vectorisation. *Parallel Computing*, 3(2):141–152, 1986.

[15] Uwe Harms and Wolfgang Rönsch. International conference on vector and parallel computing. *Parallel Computing*, 3(4):359–363, 1986.

[16] R. Hiromoto. Some issues in parallel processing as encountered on the denelcor hep. *Parallel Computing*, 3(2):111–127, 1986.

[17] Hans-Christian Hoppe and Heinz Mühlenbein. Parallel adaptive full-multigrid methods on message-based multiprocessors. *Parallel Computing*, 3(4):269–287, 1986.

[18] Harry F. Jordan. Structuring parallel algorithms in an mimd, shared memory environment. *Parallel Computing*, 3(2):93–110, 1986.

[19] E. Katona. A lattice model for cellular (systolic) algorithms. *Parallel Computing*, 3(3):251–258, 1986.

[20] Joseph W.H. Liu. Computational models and task scheduling for parallel sparse cholesky factorization. *Parallel Computing*, 3(4):327–342, 1986.

[21] U. Meier. Two parallel sor variants of the schwarz alternating procedure. *Parallel Computing*, 3(3):205–215, 1986.

[22] J.J. Modi and J.S. Rollett. Some problems of exploiting a pipeline processor. *Parallel Computing*, 3(3):263–265, 1986.

[23] W. Oed and O. Lange. Modelling, measurement, and simulation of memory interference in the cray x-mp. *Parallel Computing*, 3(4):343–358, 1986.

[24] D. Parkinson. Parallel efficiency can be greater than unity. *Parallel Computing*, 3(3):261–262, 1986.

[25] Mark K. Seager. Parallelizing conjugate gradient for the cray-x-mp. *Parallel Computing*, 3(1):35–47, 1986.

[26] John P. Shen, John P. Hayes, Luigi Ciminiera, and Angelo Serra. Fault-tolerance and performance analysis of beta-networks. *Parallel Computing*, 3(3):231–249, 1986.

[27] Michael Sonnenschein. An extension of the language c for concurrent programming. *Parallel Computing*, 3(1):59–71, 1986.

[28] Henk A. van der Vorst. The performance of fortran implementations for preconditioned conjugate gradients on vector computers. *Parallel Computing*, 3(1):49–58, 1986.

[29] C.B. Yang and R.C.T. Lee. The mapping of 2-d array processors to 1-d array processors. *Parallel Computing*, 3(3):217–229, 1986.