References

[1] John K. Antonio and Richard C. Metzger. Hypersphere mapper: A nonlinear programming approach to the hypercube embedding problem. *J. Parallel Distrib. Comput.*, 19(3):262–270, 1993.

[2] Lubomir Bic and Mayez Al-Mouhamed. The em-4 under implicit parallelism. *J. Parallel Distrib. Comput.*, 19(3):255–261, 1993.

[3] Theodore Brown and Renbing Xiong. A parallel quicksort algorithm. *J. Parallel Distrib. Comput.*, 19(2):83–89, 1993.

[4] Yi-Chieh Chang and Kang G. Shin. Optimal load sharing in distributed real-time systems. *J. Parallel Distrib. Comput.*, 19(1):38–50, 1993.

[5] Hyeong-Ah Choi and B. Narahari. Efficient algorithms for mapping and partitioning a class of parallel computations. *J. Parallel Distrib. Comput.*, 19(4):349–363, 1993.

[6] Sang Bang Choi and Arun K. Somani. Rearrangeable circuit-switched hypercube architectures for routing permutations. *J. Parallel Distrib. Comput.*, 19(2):125–130, 1993.

[7] Adair Dingle and I. Hal Sudborough. Simulation of binary trees and x-trees on pyramid networks. *J. Parallel Distrib. Comput.*, 19(2):119–124, 1993.

[8] Marco Ferretti. The generalized hough transform on mesh-connected computers. *J. Parallel Distrib. Comput.*, 19(1):51–57, 1993.

[9] G.R. Gao. An efficient hybrid dataflow architecture model. *J. Parallel Distrib. Comput.*, 19(4):293–307, 1993.

[10] Cécile Germain, Jean-Luc Béchennec, Daniel Etiemble, and Jean-Paul Sansonnet. A communication architecture for a massively parallel message-passing multicomputer. *J. Parallel Distrib. Comput.*, 19(4):338–348, 1993.

[11] D.H. Gill, T.J. Smith, T.E. Gerasch, J.V. Warren, C.L. McCreary, and R.E.K. Stirewalt. Spatial-temporal analysis of program dependence graphs for useful parallelism. *J. Parallel Distrib. Comput.*, 19(2):103–118, 1993.

[12] Ajay K. Gupta and Susanne E. Hambrusch. Multiple network embeddings into hypercubes. *J. Parallel Distrib. Comput.*, 19(2):73–82, 1993.

[13] Anshul Gupta and Vipin Kumar. Performance properties of large scale parallel systems. *J. Parallel Distrib. Comput.*, 19(3):234–244, 1993.

[14] C.-H. Huang and P. Sadayappan. Communication-free hyperplane partitioning of nested loops. *J. Parallel Distrib. Comput.*, 19(2):90–102, 1993.

[15] A. Kavianpour. A new measure in system-level diagnosis of hypercubes. *J. Parallel Distrib. Comput.*, 19(4):372–378, 1993.

[16] W.B. Ligon III and U. Ramachandran. An empirical methodology for exploring reconfigurable architectures. *J. Parallel Distrib. Comput.*, 19(4):323–337, 1993.

[17] Mengjou Lin, Rose P. Tsang, David H.C. Du, Alan E. Klietz, and Stephen Saroff. Performance characteristics of the connenction machine hypertree network. *J. Parallel Distrib. Comput.*, 19(3):245–254, 1993.

[18] Yen-Chun Lin. Perfectly overlapped generation of long runs for sorting large files. *J. Parallel Distrib. Comput.*, 19(2):136–142, 1993.

[19] Yi-Bing Lin. On terminating a distributed discrete event simulation. *J. Parallel Distrib. Comput.*, 19(4):364–371, 1993.

[20] Serge M. Manning and David G. Meyer. Analysis of asynchronous execution streams with i-caching in massively parallel systems. *J. Parallel Distrib. Comput.*, 19(3):279–291, 1993.

[21] Ashwini K. Nanda and Laxmi N. Bhuyan. Efficient mapping of applications on cache based multiprocessors. *J. Parallel Distrib. Comput.*, 19(3):179–191, 1993.

[22] David Nassimi and Yuh-Dong Tsai. An efficient implementation of batcher's odd-even merge on a simd hypercube. *J. Parallel Distrib. Comput.*, 19(1):58–63, 1993.

[23] Wayne G. Nation, Anthony A. Maciejewski, and Howard Jay Siegel. A methodology for exploiting concurrency among independent tasks in

partitionable parallel processing systems. *J. Parallel Distrib. Comput.*, 19(3):271–278, 1993.

[24] Jih-Kwon Peir and Yann-Hang Lee. Look-ahead routing switches for multistage interconnection networks. *J. Parallel Distrib. Comput.*, 19(1):1–10, 1993.

[25] Dar-Tzen Peng and Kang G. Shin. A new performance measure for scheduling independent real-time tasks. *J. Parallel Distrib. Comput.*, 19(1):11–26, 1993.

[26] Ravi Ponnusamy, Rajeev Thakur, Alok Choudhary, Kishore Velamakanni, Zeki Bozkus, and Geoffrey Fox. Experimental performance evaluation of the cm-5. *J. Parallel Distrib. Comput.*, 19(3):192–202, 1993.

[27] Sanjay Ranka, Jhy-Chun Wang, and Nangkang Yeh. Embedding meshes on the star graph. *J. Parallel Distrib. Comput.*, 19(2):131–135, 1993.

[28] Gene Saghi, Howard Jay Siegel, and Jeffery L. Gray. Predicting performance and selecting modes of parallelism: A case study using cyclic reduction on three parallel machines. *J. Parallel Distrib. Comput.*, 19(3):219–233, 1993.

[29] Xian-He Sun and Lionel M. Ni. Scalable problems and memory-bounded speedup. *J. Parallel Distrib. Comput.*, 19(1):27–37, 1993.

[30] Nian-Feng Tzeng. A reliable cube-connected cycles structure. *J. Parallel Distrib. Comput.*, 19(1):64–71, 1993.

[31] Sriram Vajapeyam and Wei-Chung Hsu. Toward effective scalar hardware for highly vectorizable applications. *J. Parallel Distrib. Comput.*, 19(3):147–162, 1993.

[32] Shlomit Weiss, Ilan Y. Spillinger, and Gabriel M. Silberman. Architectural improvements for a data-driven vlsi processing array. *J. Parallel Distrib. Comput.*, 19(4):308–322, 1993.

[33] Richard M. Wolski and John T. Feo. Program partitioning for numa multiprocessor computer systems. *J. Parallel Distrib. Comput.*, 19(3):203–218, 1993.

[34] Qing Yang. Performance of cache memories for vector computers. *J. Parallel Distrib. Comput.*, 19(3):163–178, 1993.