

References

- [1] Martín Abadi and Joseph Y. Halpern. Decidability and expressiveness for first-order logics of probability. *Inf. Comput.*, 112(1):1–36, 1994, July.
- [2] Eric Allender and Ulrich Hertrampf. Depth reduction for circuits of unbounded fan-in. *Inf. Comput.*, 112(2):217–238, 1994, August.
- [3] Hubert Comon and Catherine Delor. Equational formulae with membership constraints. *Inf. Comput.*, 112(2):167–216, 1994, August.
- [4] Yannis Dimopoulos and Vangelis Magirou. A graph-theoretic approach to default logic. *Inf. Comput.*, 112(2):239–256, 1994, August.
- [5] Vince Grolmusz. The bns lower bound for multi-party protocols is nearly optimal. *Inf. Comput.*, 112(1):51–54, 1994, July.
- [6] M. Hennessy. A fully abstract denotational model for higher-order processes. *Inf. Comput.*, 112(1):55–95, 1994, July.
- [7] Thomas A. Henzinger, Zohar Manna, and Amir Pnueli. Temporal proof methodologies for timed transition systems. *Inf. Comput.*, 112(2):273–337, 1994, August.
- [8] Christos Levcopoulos and Ola Petersson. Sorting shuffled monotone sequences. *Inf. Comput.*, 112(1):37–50, 1994, July.
- [9] Ian Parberry. A guide for new referees in theoretical computer science. *Inf. Comput.*, 112(1):96–116, 1994, July.
- [10] Ramamohan Paturi and Michael E. Saks. Approximating threshold circuits by rational functions. *Inf. Comput.*, 112(2):257–272, 1994, August.
- [11] Benjamin C. Pierce. Bounded quantification is undecidable. *Inf. Comput.*, 112(1):131–165, 1994, July.
- [12] P.Y. Yan. Exponential size lower bounds for some depth three circuits. *Inf. Comput.*, 112(1):117–130, 1994, July.