References

[1] Anant Agarwal, David A. Kranz, and Venkat Natarajan. Automatic partitioning of parallel loops and data arrays for distributed shared-memory multiprocessors. *IEEE Trans. Parallel and Distrib. Systems*, 6(9):943–962, 1995.

[2] Gagan Agrawal and Pankaj Jalote. Coding-based replication schemes for distributed systems. *IEEE Trans. Parallel and Distrib. Systems*, 6(3):240–251, 1995.

[3] Gagan Agrawal, Alan Sussman, and Joel Saltz. An integrated runtime and compile-time approach for parallelizing structured and block structured applications. *IEEE Trans. Parallel and Distrib. Systems*, 6(7):747–754, 1995.

[4] Alexander Aiken, Alexandru Nicolau, and Steven Novack. Resource-constrained software pipelining. *IEEE Trans. Parallel and Distrib. Systems*, 6(12):1248–1270, 1995.

[5] M. Sultan Alam and Rami G. Mehlhem. Routing in modular fault-tolerant multiprocessor systems. *IEEE Trans. Parallel and Distrib. Systems*, 6(11):1206–1220, 1995.

[6] Hussein M. Alnuweiri. Parallel constant-time connectivity algorithms on a reconfigrable network of processors. *IEEE Trans. Parallel and Distrib. Systems*, 6(1):105–110, 1995.

[7] M. Ashraf Iqbal and Shahid H. Bokhari. Efficient algorithms for a class of partitioning problems. *IEEE Trans. Parallel and Distrib. Systems*, 6(2):170–175, 1995.

[8] Cevdet Aykanat and Argun Derviş. Efficient fast hartley transform algorithms for hypercube-connected multicomputers. *IEEE Trans. Parallel and Distrib. Systems*, 6(6):561–577, 1995.

[9] Nader Bagherzadeh, Martin Dowd, and Shahram Latifi. A well-behaved enumeration of star graphs. *IEEE Trans. Parallel and Distrib. Systems*, 6(5):531–535, 1995.

[10] David E. Bakken and Richard D. Schlichting. Supporting fault-tolerant parallel programming in linda. *IEEE Trans. Parallel and Distrib. Systems*, 6(3):287–302, 1995.

[11] Vasanth Bala, Jehoshua Bruck, Robert Cypher, Pablo Elustondo, Alex Ho, Ching-Tien Ho, Shlomo Kipnis, and Marc Snir. Ccl: A portable and tunable collective communication library for scalable parallel computers. *IEEE Trans. Parallel and Distrib. Systems*, 6(2):154–164, 1995.

[12] Amotz Bar-Noy, Jehoshua Bruck, Ching-Tien Ho, Shlomo Kipnis, and Baruch Schieber. Computing global combine operations in the multiport postal model. *IEEE Trans. Parallel and Distrib. Systems*, 6(8):896–900, 1995.

[13] Dharmavani Bhagavathi, Venkata V. Bokka, Himabindu Gurla, Stephan Olariu, James L. Schwing, Ivan Stojmenović, and Jingyuan Zhang. Time-optimal visibility-related algorithms on meshes with multiple broadcasting. *IEEE Trans. Parallel and Distrib. Systems*, 6(7):687–703, 1995.

[14] Jan C. Bioch and Toshihide Ibaraki. Generating and approximating nondominated coteries. *IEEE Trans. Parallel and Distrib. Systems*, 6(9):905–914, 1995.

[15] Jack T. Brassil and Rene L. Cruz. Bounds on maximum delay in networks with deflection routing. *IEEE Trans. Parallel and Distrib. Systems*, 6(7):724–732, 1995.

[16] David C. Cann and Paraskevas Evripidou. Advanced array optimizations for high performance functional languages. *IEEE Trans. Parallel and Distrib. Systems*, 6(3):229–239, 1995.

[17] Bethany M.Y. Chan, Francis Y.L. Chin, and Chung-Keung Poon. Optimal simulation of full binary trees on faulty hypercubes. *IEEE Trans. Parallel and Distrib. Systems*, 6(3):269–286, 1995.

[18] Her-Kun Chang and Shyan-Ming Yuan. Performance characterization of the tree quorum algorithm. *IEEE Trans. Parallel and Distrib. Systems*, 6(6):658–662, 1995.

[19] Hua Wu David Chang and William J.B. Oldham. Dynamic task allocation models for large distributed computing systems. *IEEE Trans. Parallel and Distrib. Systems*, 6(12):1301–1315, 1995.

[20] Danny Z. Chen. Efficient geometric algorithms on the erew pram. *IEEE Trans. Parallel and Distrib. Systems*, 6(1):41–47, 1995.

[21] Danny Z. Chen. Efficient parallel binary search on sorted arrays, with applications. *IEEE Trans. Parallel and Distrib. Systems*, 6(4):440–445, 1995.

[22] Xian Chen and Graham M. Megson. A general methodology of partitioning and mapping for given regular arrays. *IEEE Trans. Parallel and Distrib. Systems*, 6(10):1100–1107, 1995.

[23] Yi-long Chen and Jyh-Charn Liu. A fault-tolerant distributed subcube management scheme for hypercube multicomputer systems. *IEEE Trans. Parallel and Distrib. Systems*, 6(7):766–772, 1995.

[24] Shun Yan Cheung and Vaidy S. Sunderam. Performance of barrier synchronization methods in a multiaccess network. *IEEE Trans. Parallel and Distrib. Systems*, 6(8):890–895, 1995.

[25] Yong-Kim Chong and Kai Hwang. Performance analysis of four memory consistency models for multithreaded multiprocessors. *IEEE Trans. Parallel and Distrib. Systems*, 6(10):1085–1099, 1995.

[26] Hong-Chich Chou and Chung-Ping Chung. An optimal instruction scheduler for superscalar processor. *IEEE Trans. Parallel and Distrib. Systems*, 6(3):303–313, 1995.

[27] Kuo-Liang Chung. Prefix computations on a generalized mesh-connected computer with multiple buses. *IEEE Trans. Parallel and Distrib. Systems*, 6(2):196–199, 1995.

[28] Fredrik Dahlgren, Michel Dubois, and Per Stenström. Sequential hardware prefetching in shared-memory multiprocessors. *IEEE Trans. Parallel and Distrib. Systems*, 6(7):733–746, 1995.

[29] Sivarama P. Dandamudi and Philip S.P. Cheng. A hierarchical task queue organization for shared-memory multiprocessor systems. *IEEE Trans. Parallel and Distrib. Systems*, 6(1):1–16, 1995.

[30] Debendra Das Sharma and Dhiraj K. Pradhan. Processor allocation in hypercube multicomputers: Fast and efficient strategies for cubic and noncubic allocation. *IEEE Trans. Parallel and Distrib. Systems*, 6(10):1108–1123, 1995.

[31] Mark Debbage, Mark B. Hill, and Denis A. Nicole. An interface to a reliable packet delivery service for parallel systems. *IEEE Trans. Parallel and Distrib. Systems*, 6(4):400–411, 1995.

[32] Nikitas J. Dimopoulos and Vassilios V. Dimakopoulos. Optimal and suboptimal processor allocation for hypercycle-based multiprocessors. *IEEE Trans. Parallel and Distrib. Systems*, 6(2):175–185, 1995.

[33] Robert T. Dimpsey and Ravishankar K. Iyer. A measurement-based model to predict the performance impact of system modifications: A case study. *IEEE Trans. Parallel and Distrib. Systems*, 6(1):28–40, 1995.

[34] José Duato. A necessary and sufficient condition for deadlock-free adaptive routing in wormhole networks. *IEEE Trans. Parallel and Distrib. Systems*, 6(10):1055–1067, 1995.

[35] José Duato. A theory of deadlock-free adaptive multicast routing in wormhole networks. *IEEE Trans. Parallel and Distrib. Systems*, 6(9):976–987, 1995.

[36] Pradeep K. Dubey, George B. Adams III, and Michael J. Flynn. Evaluating performance tradeoffs between fine-grained and coarse-grained alternatives. *IEEE Trans. Parallel and Distrib. Systems*, 6(1):17–27, 1995.

[37] Dyi-Rong Duh, Gen-Huey Chen, and Jywe-Fei Fang. Algorithms and properties of a new two-level network with folded hypercubes as basic modules. *IEEE Trans. Parallel and Distrib. Systems*, 6(7):714–723, 1995.

[38] Kemal Efe and Antonio Fernández. Products of networks with logarithmic diameter and fixed degree. *IEEE Trans. Parallel and Distrib. Systems*, 6(9):963–975, 1995.

[39] Kemal Efe and Venkatesh Krishnamoorthy. Optimal scheduling of compute-intensive tasks on a network of workstations. *IEEE Trans. Parallel and Distrib. Systems*, 6(6):668–673, 1995.

[40] Ahmed El-Amawy and Priyalal Kulasinghe. Properties of generalized branch and combine clock networks. *IEEE Trans. Parallel and Distrib. Systems*, 6(5):541–546, 1995.

[41] Agustín Fernández, José M. Llabería, and Miguel Velero-García. Loop transformation using nonunimodular matrices. *IEEE Trans. Parallel and Distrib. Systems*, 6(8):832–840, 1995.

[42] Hideo Fujiwara and Tomoo Inoue. Optimal granularity and scheme of parallel test generation in a distributed system. *IEEE Trans. Parallel and Distrib. Systems*, 6(7):677–686, 1995.

[43] Patrick T. Gaughan and Sudhakar Yalamanchili. A family of fault-tolerant routing protocols for direct multiprocessor networks. *IEEE Trans. Parallel and Distrib. Systems*, 6(5):482–497, 1995.

[44] Kanad Ghose and Kiran R. Desai. Hierarchical cubic networks. *IEEE Trans. Parallel and Distrib. Systems*, 6(4):427–435, 1995.

[45] Antonio González, Miguel Valero-García, and Luis Díaz de Cerio. Executing algorithms with hypercube topology on torus multicomputers. *IEEE Trans. Parallel and Distrib. Systems*, 6(8):803–814, 1995.

[46] Sumit Gosh and Meng-Lin Yu. An asynchronous distributed approach for the simulation of behavior-level models on parallel processors. *IEEE Trans. Parallel and Distrib. Systems*, 6(6):639–652, 1995.

[47] Qian-Ping Gu and Jun Gu. Two packet routing algorithms on a mesh-connected computer. *IEEE Trans. Parallel and Distrib. Systems*, 6(4):436–440, 1995.

[48] Anshul Gupta, Vipin Kumar, and Ahmed Sameh. Performance and scalability of preconditioned conjugate gradient methods on parallel computers. *IEEE Trans. Parallel and Distrib. Systems*, 6(5):455–469, 1995.

[49] Vipul Gupta and Eugen Schenfeld. Annealed embeddings of communication patterns in an interconnection cached network. *IEEE Trans. Parallel and Distrib. Systems*, 6(11):1153–1167, 1995.

[50] Ching-Tien Ho and Ming-Yang Kao. Optimal broadcast in all-port wormhole-routed hypercubes. *IEEE Trans. Parallel and Distrib. Systems*, 6(2):200–204, 1995.

[51] Chul-Eui Hong and Bruce M. McMillin. Relaxing synchronization in distributed simulated annealing. *IEEE Trans. Parallel and Distrib. Systems*, 6(2):189–195, 1995.

[52] Garng M. Huang and Wen-Lin Hsieh. Exact convergence of a parallel textured algorithm for data network optimal routing problems. *IEEE Trans. Parallel and Distrib. Systems*, 6(11):1132–1146, 1995.

[53] Kaiyuan Huang, Vinod K. Agarwal, Laurence LaForge, and K. Thulasiraman. A diagnosis algorithm for constant degree structures and its application to vlsi circuit testing. *IEEE Trans. Parallel and Distrib. Systems*, 6(4):363–372, 1995.

[54] Djaffer Ibaroudene and Raj Acharya. Parallel display of objects represented by linear octrees. *IEEE Trans. Parallel and Distrib. Systems*, 6(1):79–85, 1995.

[55] Byeong-Soo Jeong and Edward Omiecinski. Inverted file partitioning schemes in multiple disk systems. *IEEE Trans. Parallel and Distrib. Systems*, 6(2):142–153, 1995.

[56] Xiaohua Jia. A total ordering multicast protocol using propagation trees. *IEEE Trans. Parallel and Distrib. Systems*, 6(6):617–627, 1995.

[57] Dennis Kafura, Manibrata Mukherji, and Douglas M. Washabaugh. Concurrent and distributed garbage collection of active objects. *IEEE Trans. Parallel and Distrib. Systems*, 6(4):337–350, 1995.

[58] Edgar T. Kalns and Lionel M. Ni. Processor mapping techniques toward efficient data redistribution. *IEEE Trans. Parallel and Distrib. Systems*, 6(12):1234–1247, 1995.

[59] Tzong-Wann Kao, Shi-Jinn Horng, Yue-Li Wang, and Horng-Ren Tsai. Designing efficient parallel algorithms on crap. *IEEE Trans. Parallel and Distrib. Systems*, 6(5):554–560, 1995.

[60] Ten-Hwang Lai and Li-Fen Wu. An $(n-1)$-resilient algorithm for distributed termination detection. *IEEE Trans. Parallel and Distrib. Systems*, 6(1):63–78, 1995.

[61] Youran Lan. An adaptive fault-tolerant routing algorithm for hypercube multicomputers. *IEEE Trans. Parallel and Distrib. Systems*, 6(11):1147–1152, 1995.

[62] Ching-Yi Lee and A. Yavuz Oruç. A fast parallel algorithm for routing unicast assignments in beneš networks. *IEEE Trans. Parallel and Distrib. Systems*, 6(3):329–334, 1995.

[63] De-Lei Lee and Kenneth E. Batcher. A multiway merge sorting network. *IEEE Trans. Parallel and Distrib. Systems*, 6(2):211–215, 1995.

[64] Gyungho Lee. Parallelizing iterative loops with conditional branching. *IEEE Trans. Parallel and Distrib. Systems*, 6(2):185–189, 1995.

[65] Gyungho Lee, Byung-Chang Kang, and Richard Y. Kain. Analysis of finite buffered multistage combining networks. *IEEE Trans. Parallel and Distrib. Systems*, 6(7):760–766, 1995.

[66] Isaac Yi-Yuan Lee and Sheng-De Wang. Ring-connected networks and their relationship to cubical ring connected cycles and dynamic redundancy networks. *IEEE Trans. Parallel and Distrib. Systems*, 6(9):988–996, 1995.

[67] Soo-Young Lee and Chung-Ming Chen. Optimal hot spot allocation on meshes for large-scale data-parallel algorithms. *IEEE Trans. Parallel and Distrib. Systems*, 6(8):788–802, 1995.

[68] Ran Libeskind-Hadas, Nimish Shrivastava, Rami G. Mehlhem, and C.L. Liu. Optimal reconfiguration algorithms for real-time fault-tolerant processor arrays. *IEEE Trans. Parallel and Distrib. Systems*, 6(5):498–510, 1995.

[69] Rong Lin and Stephan Olariu. Reconfigurable buses with shift switching: Concepts and applications. *IEEE Trans. Parallel and Distrib. Systems*, 6(1):93–102, 1995.

[70] Xiaola Lin, Philip K. Mckinley, and Lionel M. Ni. The message flow model for routing in wormhole-routed networks. *IEEE Trans. Parallel and Distrib. Systems*, 6(7):755–760, 1995.

[71] Jyh-Charn Liu, Kang G. Shin, and Charles C. Chang. Prevention of congestion in packet-switched multistage interconnection networks. *IEEE Trans. Parallel and Distrib. Systems*, 6(5):535–541, 1995.

[72] K. Mani Chandy and Ian Foster. A notation for deterministic cooperating processes. *IEEE Trans. Parallel and Distrib. Systems*, 6(8):863–871, 1995.

[73] Mythili Mantharam and P.J. Eberlein. The real two-zero algorithm: A parallel algorithm to reduce a real matrix to a real schur form. *IEEE Trans. Parallel and Distrib. Systems*, 6(1):48–62, 1995.

[74] Soo-Mook Moon and Scott D. Carson. Generalized multiway branch unit for vliw micoroprocessors. *IEEE Trans. Parallel and Distrib. Systems*, 6(8):850–862, 1995.

[75] Farnaz Mounes-Toussi and David J. Lilja. The potential of compile-time analysis to adapt the cache coherence enforcement strategy to the data sharing characteristics. *IEEE Trans. Parallel and Distrib. Systems*, 6(5):470–481, 1995.

[76] Tarek M. Nabhan and Albert Y. Zomaya. A parallel simulated annealing algorithm with low communication overhead. *IEEE Trans. Parallel and Distrib. Systems*, 6(12):1226–1233, 1995.

[77] Robert H.B. Netzer and Jian Xu. Necessary and sufficient conditions for consistent global snapshots. *IEEE Trans. Parallel and Distrib. Systems*, 6(2):165–169, 1995.

[78] Ilan Newman and Assaf Schuster. Hot-potato algorithms for permutation routing. *IEEE Trans. Parallel and Distrib. Systems*, 6(11):1168–1176, 1995.

[79] Madhusudan Nigam and Sartaj Sahni. Sorting n^2 numbers on $n \times n$ meshes. *IEEE Trans. Parallel and Distrib. Systems*, 6(12):1221–1225, 1995.

[80] Santosh Pande, Dharma P. Agrawal, and Jon Mauney. A scalable scheduling scheme for functional parallelism on distributed memory multiprocessor systems. *IEEE Trans. Parallel and Distrib. Systems*, 6(4):388–399, 1995.

[81] Chan-Ik Park. Efficient placement of parity and data to tolerate two disk failures in disk array systems. *IEEE Trans. Parallel and Distrib. Systems*, 6(11):1177–1184, 1995.

[82] Fong Pong and Michel Dubois. A new approach for the verification of cache coherence protocols. *IEEE Trans. Parallel and Distrib. Systems*, 6(8):773–787, 1995.

[83] Ravi Ponnusamy, Joel Saltz, Alok Choudhary, Yuan-Shin Hwang, and Geoffrey Fox. Runtime support and compilation methods for user-specified irregular data distributions. *IEEE Trans. Parallel and Distrib. Systems*, 6(8):815–831, 1995.

[84] Karl C. Posch and Reinhard Posch. Modulo reduction in residue number systems. *IEEE Trans. Parallel and Distrib. Systems*, 6(5):449–454, 1995.

[85] Ira Pramanick and Jon G. Kuhl. An inherently parallel method for heuristic problem-solving: Part i — general framework. *IEEE Trans. Parallel and Distrib. Systems*, 6(10):1006–1015, 1995.

[86] Ira Pramanick and Jon G. Kuhl. An inherently parallel method for heuristic problem-solving: Part ii— example applications. *IEEE Trans. Parallel and Distrib. Systems*, 6(10):1016–1028, 1995.

[87] Gill A. Pratt and John Nguyen. Distributed synchronous clocking. *IEEE Trans. Parallel and Distrib. Systems*, 6(3):314–328, 1995.

[88] Cosimo Antonio Prete, Gianpaolo Prina, and Luigi Ricciardi. A trace-driven simulator for performance evaluation of cache-based multiprocessor systems. *IEEE Trans. Parallel and Distrib. Systems*, 6(9):915–929, 1995.

[89] William Pugh and David Wonnacott. Going beyond integer programming with the omega test to eliminate false data dependences. *IEEE Trans. Parallel and Distrib. Systems*, 6(2):204–211, 1995.

[90] Suresh Rai, Jerry L. Trahan, and Thomas Smailus. Processor allocation in hypercube multiprocessors. *IEEE Trans. Parallel and Distrib. Systems*, 6(6):606–616, 1995.

[91] Krithi Ramamritham. Allocation and scheduling of precedence-related periodic tasks. *IEEE Trans. Parallel and Distrib. Systems*, 6(4):412–420, 1995.

[92] Parameswaran Ramanathan and Suresh Chalasani. Resource placement with multiple adjacency constraints in k-ary n-cubes. *IEEE Trans. Parallel and Distrib. Systems*, 6(5):511–519, 1995.

[93] Sampath Rangarajan, Sanjeev Setia, and Satish K. Tripathi. A fault-tolerant algorithm for replicated data management. *IEEE Trans. Parallel and Distrib. Systems*, 6(12):1271–1282, 1995.

[94] Nageswara S.V. Rao, Kurt J. Maly, Stephan Olariu, Sudheer Dharanikota, Liping Zhang, and David E. Game. Average waiting time profiles of uniform distributed queue dual bus system model. *IEEE Trans. Parallel and Distrib. Systems*, 6(10):1068–1084, 1995.

[95] David F. Robinson, Philip K. McKinley, and Betty H.C. Cheng. Optimal multicast communication in wormhole routed torus networks. *IEEE Trans. Parallel and Distrib. Systems*, 6(10):1029–1042, 1995.

[96] Seung-Woo Seo and Tse-Yun Feng. The composite banyan network. *IEEE Trans. Parallel and Distrib. Systems*, 6(10):1043–1054, 1995.

[97] Shisheng Shang and Kai Hwang. Distributed hardwired barrier synchronization for scalable multiprocessor clusters. *IEEE Trans. Parallel and Distrib. Systems*, 6(6):591–605, 1995.

[98] Fong-Chih Shao and A. Yavuz Oruç. Efficient nonblocking switching networks for interprocessor communications in multiprocessor systems. *IEEE Trans. Parallel and Distrib. Systems*, 6(2):132–141, 1995.

[99] Jang-Ping Sheu, Chao-Tsung Wu, and Tzung-Shi Chen. An optimal broadcasing algorithm without message redundancy in star graphs. *IEEE Trans. Parallel and Distrib. Systems*, 6(6):653–658, 1995.

[100] Kang G. Shin and Qin Zheng. Fddi-m: A scheme to double fddi's ability of supporting synchronous traffic. *IEEE Trans. Parallel and Distrib. Systems*, 6(11):1125–1131, 1995.

[101] Wei Shu and Min-You Wu. Asynchronous problems on simd parallel computers. *IEEE Trans. Parallel and Distrib. Systems*, 6(7):704–713, 1995.

[102] Andrew Sohn. Parallel n-ary speculative computation of simulated annealing. *IEEE Trans. Parallel and Distrib. Systems*, 6(10):997–1005, 1995.

[103] L.M. Stauffer and D.S. Hirschberg. Systolic self-organizing lists under transpose. *IEEE Trans. Parallel and Distrib. Systems*, 6(1):102–105, 1995.

[104] Jaspal Subhlok and Ken Kennedy. Integer programming for array subscript analysis. *IEEE Trans. Parallel and Distrib. Systems*, 6(6):662–668, 1995.

[105] Xian-He Sun and Jianping Zhu. Performance considerations of shared virtual memory machines. *IEEE Trans. Parallel and Distrib. Systems*, 6(11):1185–1194, 1995.

[106] Walid R. Tout and Sakti Pramanik. Distributed load balancing for parallel main memory hash join. *IEEE Trans. Parallel and Distrib. Systems*, 6(8):841–849, 1995.

[107] Jong-Chuang Tsay and Pen-Yuang Chang. Design of efficient regular arrays for matrix multiplication by two-step regularization. *IEEE Trans. Parallel and Distrib. Systems*, 6(2):215–222, 1995.

[108] Jong-Chuang Tsay and Pen-Yuang Chang. Some new designs of 2-d array for matrix multiplication and transitive closure. *IEEE Trans. Parallel and Distrib. Systems*, 6(4):351–362, 1995.

[109] Maheshwar Umasankar and Ahmed El-Amawy. Generalized algorithms for systematic synthesis of branch-and combine clock networks for meshes, tori, and hypercubes. *IEEE Trans. Parallel and Distrib. Systems*, 6(12):1283–1300, 1995.

[110] Emmanouel A. Varvarigos and Dimitri P. Bertsekas. Dynamic broadcasting in parallel computing. *IEEE Trans. Parallel and Distrib. Systems*, 6(2):120–131, 1995.

[111] Hwang-Cheng Wang and Kai Hwang. Multicoloring of grid-structured pde solvers on shared-memory multiprocessors. *IEEE Trans. Parallel and Distrib. Systems*, 6(11):1195–1205, 1995.

[112] Mu-Cheng Wang, Howard Jay Siegel, Mark A. Nichols, and Seth Abraham. Using a multipath network for reducing the effects of hot spots. *IEEE Trans. Parallel and Distrib. Systems*, 6(3):252–268, 1995.

[113] S.C. Wang, Y.H. Chin, and K.Q. Yan. Byzantine agreement in a generalized connected network. *IEEE Trans. Parallel and Distrib. Systems*, 6(4):420–427, 1995.

[114] Yi-Min Wang, Pi-Yu Chung, In-Jen Lin, and W. Kent Fuchs. Checkpoint space reclamation for uncoordinated checkpointing in message-passing systems. *IEEE Trans. Parallel and Distrib. Systems*, 6(5):546–554, 1995.

[115] Joel L. Wolf, John Turek, Ming-Syan Chen, and Philip S. Yu. A hierarchical approach to parallel multiquery scheduling. *IEEE Trans. Parallel and Distrib. Systems*, 6(6):578–590, 1995.

[116] Yung-Chang Wong, Shu-Yuen Hwang, and Jason Yi-Bing Lin. A parallelism analyzer for conservative parallel simulation. *IEEE Trans. Parallel and Distrib. Systems*, 6(6):628–638, 1995.

[117] Yuguang Wu and Richard Muntz. Stack evaluation of arbitrary set-associative multiprocessor caches. *IEEE Trans. Parallel and Distrib. Systems*, 6(9):930–942, 1995.

[118] Qing Yang and Tao Yang. A memory interference model for regularly patterned multiple stream vector accesses. *IEEE Trans. Parallel and Distrib. Systems*, 6(5):520–530, 1995.

[119] Hee Yong Youn and Youngsong Mun. On multistage interconnection networks with small clock cycles. *IEEE Trans. Parallel and Distrib. Systems*, 6(1):86–93, 1995.

[120] Chansu Yu and Chita R. Das. Disjoint task allocation algorithms for min machines with minimal conflicts. *IEEE Trans. Parallel and Distrib. Systems*, 6(4):373–387, 1995.

[121] Shyan-Ming Yuan and Her-Kun Chang. Message complexity of the tree quorum algorithm. *IEEE Trans. Parallel and Distrib. Systems*, 6(8):887–890, 1995.

[122] Xiaodong Zhang and Yong Yan. Comparative modeling and evaluation of cc-numa and coma on hierarchical ring architectures. *IEEE Trans. Parallel and Distrib. Systems*, 6(12):1316–1331, 1995.

[123] Xiaodong Zhang, Yong Yan, and Robert Castañeda. Comparative performance evaluation of hot spot contention between min-based and ring-based shared-memory architectures. *IEEE Trans. Parallel and Distrib. Systems*, 6(8):872–886, 1995.

[124] Qin Zheng and Kang G. Shin. Establishment of isolated failure immune real-time channels in harts. *IEEE Trans. Parallel and Distrib. Systems*, 6(2):113–119, 1995.

[125] Qin Zheng and Kang G. Shin. Synchronous bandwidth allocation in fddi networks. *IEEE Trans. Parallel and Distrib. Systems*, 6(12):1332–1338, 1995.