

References

- [1] S.B. Akers. A rectangular logic array. *IEEE Trans. Comput.*, C-21:848–857, 1972.
- [2] S. Bandyopadhyay, S. Basu, and A.K. Choudhary. A cellular permuter array. *IEEE Trans. Comput.*, C-21:1116–1119, 1972.
- [3] W.R. Cyre and G.J. Lipovski. On generating multipliers for a cellular fast fourier transform processor. *IEEE Trans. Comput.*, C-21:83–87, 1972.
- [4] K. Haradh. Sequential permutation networks. *IEEE Trans. Comput.*, C-21:472–479, 1972.
- [5] H.S. Stone. Dynamic memories with enhanced data access. *IEEE Trans. Comput.*, C-21:359–366, 1972.