

References

- [1] S. Abraham and K. Padmanabhan. Performance of the direct binary n -cube network for multiprocessors. *IEEE Trans. Comput.*, 38(7):1000–1011, 1989, July.
- [2] S.B. Akers and B. Krishnamurthy. A group-theoretic model for symmetric interconnection networks. *IEEE Trans. Comput.*, 38(4):555–566, 1989, April.
- [3] I.F. Akyildiz. Product form approximations for queueing networks with multiple servers and blocking. *IEEE Trans. Comput.*, 38(1):99–114, 1989, January.
- [4] Gita Alaghand and Harry F. Jordan. Sparse gaussian elimination with controlled fill-in on a shared memory multiprocessor. *IEEE Trans. Comput.*, 38(11):1539–1557, 1989, November.
- [5] Hussein G. Badr and Sunil Podar. An optimal shortest-path routing policy for network computers with regular mesh-connected topologies. *IEEE Trans. Comput.*, 38(10):1362–1371, 1989, October.
- [6] P. Banerjee and A. Dugar. The design, analysis and simulation of a fault-tolerant interconnection network supporting the fetch-and-add primitive. *IEEE Trans. Comput.*, 38(1):30–46, 1989, January.
- [7] Gianfranco Bilardi. Merging and sorting networks with the topology of the omega network. *IEEE Trans. Comput.*, 38(10):1396–1403, 1989, October.
- [8] James T. Blake and Kishor S. Trivedi. Multistage interconnection network reliability. *IEEE Trans. Comput.*, 38(11):1600–1604, 1989, November.
- [9] Guy E. Blelloch. Scans as primitive parallel operations. *IEEE Trans. Comput.*, 38(11):1526–1538, 1989, November.
- [10] V. Cherkassky and M. Malek. Partitioning and permuting properties of cc-banyan networks. *IEEE Trans. Comput.*, 38(2):274–278, 1989, February.

- [11] D.L. Eager, J. Zahorjan, and E.D. Lazowska. Speedup versus efficiency in parallel systems. *IEEE Trans. Comput.*, 38(3):408–423, 1989, March.
- [12] Gui-Liang Feng. A vlsi architecture for fast inversion in $gf(2^m)$. *IEEE Trans. Comput.*, 38(10):1383–1386, 1989, October.
- [13] H. Fleisher, J. Giraldi, R. Phoenix, and M. Tavel. Minimizability of random boolean functions. *IEEE Trans. Comput.*, 38(4):593–595, 1989, April.
- [14] M. Fürer and K. Mehlhorn. at^2 -optimal galois field multiplier for vlsi. *IEEE Trans. Comput.*, 38(9):1333–1336, 1989, September.
- [15] I. Gazit and M. Malek. On the number of permutations performable by extra-stage multistage interconnection networks. *IEEE Trans. Comput.*, 38(2):297–302, 1989, February.
- [16] K. Hwang, P.-S. Tseng, and D. Kim. An orthogonal multiprocessor for parallel scientific computations. *IEEE Trans. Comput.*, 38(1):47–61, 1989, January.
- [17] S.L. Johnsson and C.-T. Ho. Optimum broadcasting and personalized communication in hypercubes. *IEEE Trans. Comput.*, 38(9):1249–1268, 1989, September.
- [18] Adam Kapralski. The maximum and minimum selector selram and its application for developing fast sorting machines. *IEEE Trans. Comput.*, 38(11):1572–1577, 1989, November.
- [19] Gyungho Lee. A performance bound of multistage combining networks. *IEEE Trans. Comput.*, 38(10):1387–1395, 1989, October.
- [20] W. Lin, T.-L. Sheu, C.R. Das, T.-Y. Feng, and C.-L. Wu. A conflict free routing scheme on multistage interconnection networks. *IEEE Trans. Comput.*, 38(8):1086–1097, 1989, August.
- [21] L.C. Liu and H.C. Du. A near-optimal heuristic algorithm for single-row routing. *IEEE Trans. Comput.*, 38(4):603–608, 1989, April.
- [22] David M. Mandelbaum. On iterative arrays for the euclidean algorithm over finite fields. *IEEE Trans. Comput.*, 38(10):1473–1478, 1989, October.

- [23] R. Miller and Q.F. Stout. Mesh computer algorithms for computational geometry. *IEEE Trans. Comput.*, 38(3):321–340, 1989, March.
- [24] A. Mukherjee. Hardware algorithms for determining similarity between two strings. *IEEE Trans. Comput.*, 38(4):600–603, 1989, April.
- [25] Nicholas J. Naclerio, Sumio Masuda, and Kazuo Nakajima. The via minimization problem is *np*-complete. *IEEE Trans. Comput.*, 38(11):1604–1608, 1989, November.
- [26] T. Nakatani, S.-T. Huang, B.W. Arden, and S.K. Tripathi. *k*-way bitonic sort. *IEEE Trans. Comput.*, 38(2):283–288, 1989, February.
- [27] M.R. Samatham and D.K. Pradhan. The de bruijn multiprocessor network: A versatile parallel processing and sorting network for vlsi. *IEEE Trans. Comput.*, 38(4):567–581, 1989, April. see Correction in *IEEE Trans. Comput.* 40, 122-123.
- [28] I. Scherson and S. Sen. Parallel sorting in two-dimensional vlsi models of computation. *IEEE Trans. Comput.*, 38(2):238–249, 1989, February.
- [29] Weicheng Shen and A. Yavuz Oruç. On systolic contractions of program graphs. *IEEE Trans. Comput.*, 38(10):1451–1457, 1989, October.
- [30] B.P. Sinha and P.K. Srimani. Fast parallel algorithms for binary multiplication and their implementation on systolic architectures. *IEEE Trans. Comput.*, 38(3):424–431, 1989, March.
- [31] V.K. Vaishnavi. Multidimensional balanced binary trees. *IEEE Trans. Comput.*, 38(7):968–985, 1989, July.
- [32] Allen van Gelder. Pram processor allocation: A hidden bottleneck in sublogarithmic algorithms. *IEEE Trans. Comput.*, 38(2):289–292, 1989, February.
- [33] A. Varma and C.S. Raghavendra. Fault-tolerant routing in multistage interconnection networks. *IEEE Trans. Comput.*, 38(3):385–393, 1989, March.
- [34] Charles C. Wang. An algorithm to design finite field multipliers using a self-dual normal basis. *IEEE Trans. Comput.*, 38(10):1457–1460, 1989, October.